Fig. 1

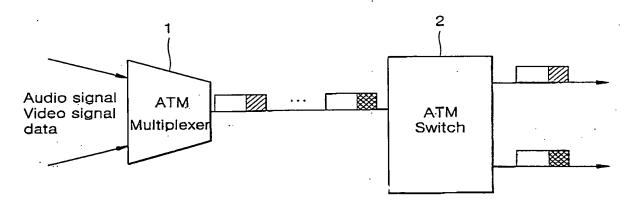
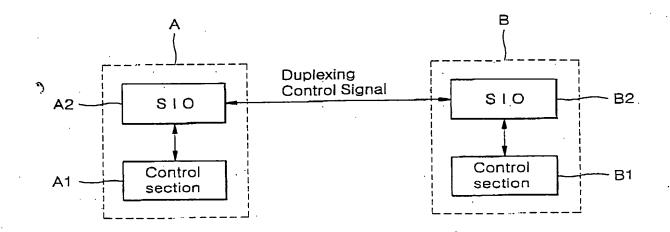


Fig. 2



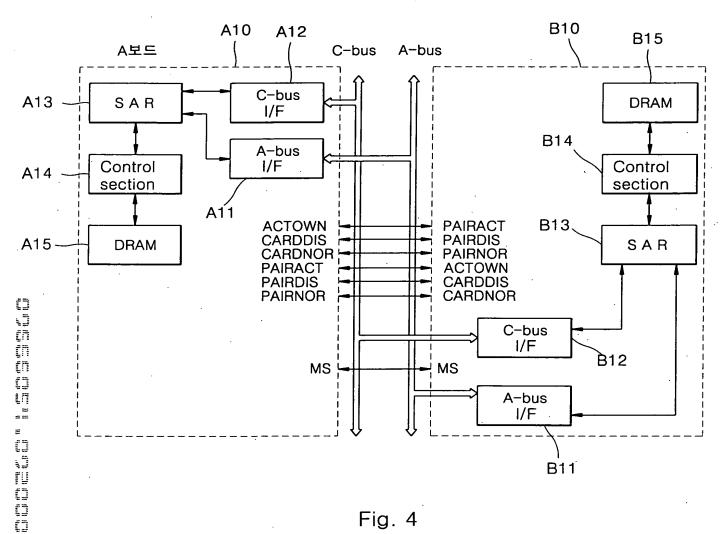


Fig. 4

		·	_ 1
Header (5byte)			
Node	Slot	Port	
VP			$\neg \setminus$
VP			\ .
. VC			
. VC] \
Node	Slot	Port	payload
. VP			
VP ·			\neg
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VC			
1	ction Related ir width, cell typ		